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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10045036	FILING DATE 01/15/2002	CLASS 439 ²⁹	SUBCLASS 832	GAU 2833	EXAMINER Phan
**APPLICANTS: Tanaka Toshihiko; Kubo Masaharu; Hattori Takashi;					
**CONTINUING DATA VERIFIED: No TM					
** FOREIGN APPLICATIONS VERIFIED: Yes TM JAPAN 2001-019774 01/29/2001					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials T.P.				ATTORNEY DOCKET NO XA-9613	
TITLE : Wiring substrate manufacturing method					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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